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transformation

Frank Vahid

Proceedings of European design automation conference with
EURO-VHDL '95 on EURO-DAC '95 with EURO-VHDL '95 December
1995

2 Partitioning sequential programs for CAD using a three-step 84%

approach

Frank Vahid

ACM Transactions on Design Automation of Electronic Systems
(TODAES) July 2002
Volume 7 Issue 3

Many computer-aided design problems involve solutions that require the partitioning of a large sequential program written in a language such as C or VHDL. Such partitioning can improve design metrics such as performance, power, energy, size, input/output lines, and even CAD tool run-time and memory requirements, by partitioning among hardware modules, hardware and software processors, or even among time-slices in reconfigurable computing devices. Previous partitioning approaches typically presel ...

3 A three-step approach to the functional partitioning of large 77%

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|  | Andrew S. Tanenbaum , Hans van Staveren , Johan W. Stevenson
ACM Transactions on Programming Languages and Systems (TOPLAS)
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|  | C. W. Fraser
ACM SIGPLAN Notices , Proceedings of the SIGPLAN '89 Conference
on Programming language design and implementation June 1989
Volume 24 Issue 7 | |
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|  | Jack W. Davidson , Christopher W. Fraser
ACM Transactions on Programming Languages and Systems (TOPLAS)
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